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200		Application Number	10/787, 116
	MINFORMATION DISCLOSURE	Filing Date	Feb. 27, 2004
	STATEMENT BY APPLICANT	First Named Inventor	Tadahiko Hisano
V.3		Group Art Unit	2182
	(use as many sheets as necessary)	Examiner Name	Rehana Perveen

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Examiner Initials	Cite No.1	U.S. Patent Number	Document Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	cl/subel
DES		5319755		Farmwald, et al.	07/07/1994	
Sel		5513327		Farmwald, et al.	04/30/1996	
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A		5841580		Farmwald, et al.	11/24/1998	
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54	 JP	A-7-175747		Hiroo Mine	07/14/1995		
SP	JP	A-7-028741		Toshiba Corp.	01/31/1995		
500	 JP	U-6-25938		Nikisou Co., Ltd	04/08/1994	3	
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OSP	 JP	A-7-56659		NTT DATA TSUSHIN KK	03/03/1995		
06	 JP	A-63-29871		Sharp Corp.	02/08/1988		
 	 						
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1			A-60-215267 A-63-110947		Kang K K	10/28/1985	
		IP.	A-6-149431		NEC Corp.	07/16/1999	
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A C		80186/80188 Intel User's Manual (Nov., 1994).	
B		P. Oguic, "Carte E/S externe pour port parallel", Electronique Radio Plans, No. 567, Feb. 1, 1994, pp. 43-47.	-
S		B. J. Freeman et al.,: "Microprocessor-to-microprocessor communications via an 8-bit data bus" IBM Technical Disclosure Bulletin., vol. 25, No. 10, Mar. 1983, pp. 5230-5235.	-
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